**Homework 8**

**Computer Architecture**

1. Construct a 5-to-32 line decoder with four 3-to-8 line decoder with enable and one 2-to-4 line decoder.
Use block diagrams.
2. Given a 32 × 8 ROM chip with an enable input, show the external connections necessary to construct a 128 × 8 ROM with four chips and a decoder.